

PTO	
9/617518	
07-11-00	
1251	Subclass
Y74	Class
ISSUE CLASSIFICATION	

PATE

U.S. UTILITY Patent Application

12W O.I.P.E.
SCANNED *MS(3)* Q.A.

PATENT DATE

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/617518	D	505		1751	

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TITLE
Enhanced high temperature coated superconductors architecture

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
Y74	1251	505	231	237	926
INTERNATIONAL CLASSIFICATION H01B 12/00					
<input type="checkbox"/> Continued on Issue Slip Inside File					

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOW	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Cls
	5	9	10,20	31	1
NOTICE OF ALLOWANCE					
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<i>Handwritten signature</i>			8/7/02	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____					
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.					
KAMAND CUNEO PRIMARY EXAMINER (Primary Examiner) 7/29/02					
J. McInroy (Legal Instruments Examiner) 9/4/02					
ISSUE FEE					
Amount Due 128.00 Date					
ISSUE BATCH NUME					

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